

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1 (withdrawn). A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

wherein each of said plurality of pixels comprises an EL element; a first EL driver TFT and a second EL driver TFT for controlling light emitted from said EL element; a switching TFT for controlling said first EL driver TFT and said second EL driver TFT; and an erasure TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein said switching TFT is controlled by said first gate signal line driver circuit;

wherein said erasure TFT is controlled by said second gate signal line driver circuit; and

wherein said EL element is controlled by said switching TFT or the erasure TFT.

2 (withdrawn). A light emitting device according to of claim 1, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

3 (withdrawn). A light emitting device according to claim 1, wherein said switching TFT, said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a top gate TFT.

4 (withdrawn). A light emitting device according to claim 1, wherein said switching TFT, said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a bottom gate TFT.

5 (withdrawn). A light emitting device according to claim 1, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

6 (withdrawn). A light emitting device according to claim 5, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

7 (withdrawn). A light emitting device according to claim 1, wherein said first EL driver TFT or said second EL driver TFT is driven in a linear region.

8 (withdrawn). A light emitting device according to claim 1, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

9 (withdrawn). A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

wherein each of said plurality of pixels comprises an EL element; a first EL driver TFT and a second EL driver TFT for controlling light emitted from said EL element; a switching TFT for controlling said first EL driver TFT and said second EL driver TFT; and an erasure TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein said switching TFT is controlled by said first gate signal line driver circuit;

wherein said erasure TFT is controlled by said second gate signal line driver circuit; and

wherein a period during said EL element emits light is controlled by said switching TFTs or said erasure TFTs to perform a gray scale display.

10. (Currently amended) A light emitting device comprising a source signal line driver

circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element~~[[;]]~~, a switching TFT~~[[;]]~~, an erasure TFT~~[[;]]~~, a first EL driver TFT~~[[;]]~~, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is controlled by electrically connected with a first gate signal line output from ~~connected with~~ said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is controlled by electrically connected with a second gate signal line output from ~~connected with~~ said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT are controlled by is electrically connected with one of source and drain regions of said switching TFT ~~or~~ and one of source and drain regions of said erasure TFT; and

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT, and

~~wherein said first EL driver TFT and said second EL driver TFT are electrically connected with said switching TFT.~~

11. (Currently amended) A light emitting device according to ~~of~~ claim 10, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

12. (Original) A light emitting device according to claim 10, wherein said switching TFT,

said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a top gate TFT.

13. (Original) A light emitting device according to claim 10, wherein said switching TFT, said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a bottom gate TFT.

14. (Original) A light emitting device according to claim 10, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

15. (Original) A light emitting device according to claim 14, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

16. (Original) A light emitting device according to claim 10, wherein said first EL driver TFT or said second EL driver TFT is driven in a linear region.

17. (Original) A light emitting device according to claim 10, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

18 (withdrawn). A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element; a switching TFT; an erasure TFT; a first EL driver TFT; and a second driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;  
wherein said switching TFT is controlled by a first gate signal output from said first gate signal line driver circuit;

wherein said erasure TFT is controlled by a second gate signal output from said second gate signal line driver circuit;

wherein said first EL driver TFT and said second EL driver TFT are controlled by said switching TFT or said erasure TFT; and

wherein a period during which said EL element emits light is controlled by said first EL driver TFT and said second EL driver TFT to perform a gray scale display.

19 (withdrawn). A light emitting device comprising: a source signal line driver circuit; a first gate signal line driver circuit; a second gate signal line driver circuit; a pixel portion; a plurality of source signal lines connected to said source signal line driver circuit; a plurality of first gate signal lines connected to said first gate signal line driver circuit; a plurality of second gate signal lines connected to said second gate signal line driver circuit; and a plurality of power source supply lines;

wherein said pixel portion comprises a plurality of pixels;

wherein each of said plurality of pixels comprises a switching TFT; a first EL driver TFT; a second EL driver TFT; an erasure TFT; and an EL element;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is connected to one of said plurality of first gate signal lines;

wherein one of a source region and a drain region of the switching TFT is connected to one of said plurality of source signal lines, and the other of said source region and said drain region is

connected to a gate electrode of said first EL driver TFT and to a gate electrode of said second EL driver TFT;

wherein a gate electrode of said erasure TFT is connected to one of said plurality of second gate signal lines;

wherein one of a source region and a drain region of said erasure TFT is connected to one of said plurality of power source supply lines, and the other of said source region and said drain region is connected to said gate electrode of said first EL driver TFT and to said gate electrode of said second EL driver TFT;

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT are connected to said power source supply line; and

wherein a drain region of said first EL driver TFT and a drain region of said second EL driver TFT are connected to said EL element.

20 (withdrawn). A light emitting device according to claim 19, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

21 (withdrawn). A light emitting device according to claim 19, wherein said switching TFT, said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a top gate TFT.

22 (withdrawn). A light emitting device according to claim 19, wherein said switching TFT, said erasure TFT, said first EL driver TFT, or said second EL driver TFT is a bottom gate TFT.

23 (withdrawn). A light emitting device according to claim 19, wherein said source signal line

driver circuit comprises a shift register, a first latch, and a second latch.

24 (withdrawn). A light emitting device according to claim 19, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

25 (withdrawn). A light emitting device according to claim 19, wherein said first EL driver TFT or said second EL driver TFT is driven in a linear region.

26 (withdrawn). A light emitting device according to claim 19, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

27 (withdrawn). A light emitting device according to claims 19,  
wherein said light emitting device comprises a gate wiring connected to said gate electrode of said first EL driver TFT and to said gate electrode of said second EL driver TFT; and a capacitor wiring connected to said power source supply line; and

wherein a gate insulating film of said switching TFT, said erasure TFT, said first EL driver TFT, and said second EL driver TFT is provided between said gate wiring and said capacitor wiring.

28 (withdrawn). A light emitting device according claim 19,  
wherein, from among said plurality of pixels, two pixels along the direction in which said plurality of first gate signal lines are formed, are adjacent with one of said plurality of power source supply lines therebetween; and

wherein a source region of said first EL driver TFT and a source region of said second EL

driver TFT of each of said two pixels are connected to said power source supply line.

29 (withdrawn). A light emitting device according to claim 19,

wherein two pixels along the direction in which said plurality of first gate signal lines are formed, are adjacent with one of said plurality of second gate signal lines therebetween;

wherein a gate electrode of said first EL driver TFT and a gate electrode of said second EL driver TFT of each of said two pixels are connected to said plurality of second gate signal line; and

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT of each of said two pixels are connected to one of said plurality of power source supply lines.

30 (withdrawn). A light emitting device according to claim 19, wherein said plurality of first gate signal lines and said plurality of second gate signal lines are formed in parallel.

31 (withdrawn). A light emitting device according to claim 30, wherein said plurality of first gate signal lines and said plurality of second gate signal lines overlap with an insulating film therebetween.

32 (withdrawn). A light emitting device according to claim 19, wherein said plurality of source signal lines and said plurality of power source supply lines are formed in parallel.

33 (withdrawn). A light emitting device according to claim 32, wherein said plurality of source signal lines and the plurality of power source supply lines overlap with an insulating film



therebetween.

34 (withdrawn). A light emitting device according to claim 19, wherein said plurality of first gate signal lines and said plurality of power source supply lines are formed in parallel.

35 (withdrawn). A light emitting device according to claim 34, wherein said plurality of first gate signal lines and said plurality of power source supply lines overlap with an insulating film therebetween.

36 (withdrawn). A light emitting device according to claim 19, wherein said plurality of second gate signal lines and said plurality of power source supply lines are formed in parallel.

37 (withdrawn). A light emitting device according to claim 36, wherein said plurality of second gate signal lines and said plurality of power source supply lines overlap with an insulating film therebetween.

38-60 (Canceled)

61 (withdrawn). A light emitting device comprising: a source signal line driver circuit; a first gate signal line driver circuit; a second gate signal line driver circuit; a pixel portion; a plurality of source signal lines connected to the source signal line driver circuit; a plurality of first gate signal lines connected to the first gate signal line driver circuit; a plurality of second gate signal lines connected to the second gate signal line driver circuit; and a plurality of power source supply lines;

wherein said pixel portion comprises a plurality of pixels;

wherein each of said plurality of pixels comprises a switching TFT; a first EL driver TFT; a second EL driver TFT; an erasure TFT; and an EL element;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein said EL element comprises a pixel electrode, an opposing electrode maintained at a constant electric potential, and an EL layer provided between said pixel electrode and said opposing electrode;

wherein a gate electrode of said switching TFT is connected to one of said plurality of first gate signal lines;

wherein one of a source region and a drain region of the switching TFT is connected to one of said plurality of source signal lines, and the other of said source region and said drain region is connected to a gate electrode of said first EL driver TFT and to a gate electrode of said second EL driver TFT;

wherein a gate electrode of said erasure TFT is connected to one of said plurality of second gate signal lines;

wherein one of a source region and a drain region of said erasure TFT is connected to one of said plurality of power source supply lines, and the other of said source region and said drain region is connected to said gate electrode of said first EL driver TFT and to said gate electrode of said second EL driver TFT;

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT are connected to said power source supply line; and

wherein a drain region of said first EL driver TFT and a drain region of said second EL driver TFT are connected to said pixel electrode of said EL element.

62 (withdrawn). A light emitting device according to claim 61, wherein said EL layer is formed of a low-molecular organic material or a polymer organic material.

63 (withdrawn). A light emitting device according to claim 61, wherein said low-molecular organic material is made of Alq<sub>3</sub> (tris-8-quinolilite-aluminium) or TPD (triphenylamine derivative).

64 (withdrawn). A light emitting device according to claim 61, wherein said polymer organic material is made of PPV (polyphenylene vinylene), PVK (polyvinylcarbazole) or polycarbonate.

65 (withdrawn). A light emitting device according to claim 61, wherein said first EL driver TFT and said second EL driver TFT are p-channel TFTs when said pixel electrode is an anode.

66 (withdrawn). A light emitting device according to claim 61, wherein said first EL driver TFT and said second EL driver TFT are n-channel TFTs when said pixel electrode is a cathode.

67 (withdrawn). A light emitting device according to claim 61,  
wherein said pixel electrode and said drain region of said first EL driver TFT, and said pixel electrode and said drain region of said second EL driver TFT are connected through at least one wiring; and

wherein a bank is formed on a region where said pixel electrode is connected to at least said one wiring.

68 (withdrawn). A light emitting device according to claim 61, wherein said bank has a light shielding property.

69-75 (canceled)

76 (withdrawn). A light emitting device comprising a plurality of pixels, each of said plurality of pixels comprising a source signal line; a first gate signal line; a second gate signal line; an power source supply line; a switching TFT; a first EL driver TFT; a second EL driver TFT; an erasure TFT; and an EL element;

wherein a gate electrode of said switching TFT is connected to the first gate signal line;

wherein one of a source region and a drain region of said switching TFT is connected to said source signal line, and the other of one of said source region and said drain region is connected to a gate electrode of said first EL driver TFT and to a gate electrode of said second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said erasure TFT is connected to said second gate signal line;

wherein one of a source region and a drain region of said erasure TFT is connected to said power source supply line, and the other of one of said source region and said drain region is connected to said gate electrode of said first EL driver TFT and said gate electrode of said second EL driver TFT;

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT are connected to said power source supply line; and

wherein a drain region of said first EL driver TFT and a drain region of said second EL driver TFT are connected to said EL element.

77 (withdrawn). A light emitting device according to claim 76, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

78 (Withdrawn). A light emitting device comprising: a source signal line driver circuit; a first gate signal line driver circuit; a second gate signal line driver circuit; a pixel portion; a plurality of source signal lines connected to said source signal line driver circuit; a plurality of first gate signal lines connected to said first gate signal line driver circuit; a plurality of second gate signal lines connected to said second gate signal line driver circuit; and a plurality of power source supply lines; wherein said pixel portion comprises a plurality of pixels;

wherein each of said plurality of pixels comprises a switching TFT, a first EL driver TFT, a second EL driver TFT, an erasure TFT, and an EL element;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is electrically connected to said first gate signal line driver circuit through at least one of said plurality of first gate signal lines;

wherein one of a source region and a drain region of the switching TFT is electrically connected to said source signal line driver circuit through at least one of said plurality of source signal lines, and the other of said source region and said drain region is electrically connected to a gate electrode of said first EL driver TFT through at least a gate electrode of said second EL driver TFT;

wherein a gate electrode of said erasure TFT is electrically connected to said second gate signal line driver circuit through at least one of said plurality of second gate signal lines;

wherein a gate electrode of said erasure TFT is electrically connected to said second gate signal line driver circuit through at least one of said plurality of second gate signal lines;

wherein one of a source region and a drain region of said erasure TFT is electrically connected to one of said plurality of power source supply lines, and the other of said source region and said drain region is electrically connected to said gate electrode of said first EL driver TFT through at least said gate electrode of said second EL driver TFT;

wherein each of a source region of said first EL driver TFT and a source region of said second EL driver TFT is electrically connected to said power source supply line; and

wherein each of a drain region of said first EL driver TFT and a drain region of said second EL driver TFT is electrically connected to said EL element.

79 (Withdrawn). A light emitting device according to claim 78, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

80 (Withdrawn). A light emitting device according to claim 78, wherein at least one TFT, selected from the group consisting of said switching TFT, said erasure TFT, said first EL driver TFT, and said second EL driver TFT, is a top gate TFT.

81 (Withdrawn). A light emitting device according to claim 78, wherein at least one TFT, selected from the group consisting of said switching TFT, said erasure TFT, said first EL driver TFT, and said second EL driver TFT, is a bottom gate TFT.

82 (Withdrawn). A light emitting device according to claim 78, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

83 (Withdrawn). A light emitting device according to claim 82, wherein one latch selected from the group consisting of said first latch and said second latch comprises two clocked invertors and two invertors.

84 (Withdrawn). A light emitting device according to claim 78, wherein one EL driver selected from the group consisting of said first EL driver TFT and said second EL driver TFT is driven in a linear region.

85 (Withdrawn). A light emitting device according to claim 78, wherein said light emitting device is a device selected from the group consisting of a computer, a video camera, and a DVD player.

86 (Withdrawn). A light emitting device according to claim 78,  
wherein said light emitting device comprises a gate wiring connected to said gate electrode of said first EL driver TFT and to said gate electrode of said second EL driver TFT, and a capacitor wiring connected to said power source supply line; and

wherein a gate insulating film of said switching TFT, said erasure TFT, said first EL driver TFT, and said second EL driver TFT is provided between said gate wiring and said capacitor wiring.

87 (Withdrawn). A light emitting device according to claim 78,

wherein, from among said plurality of pixels, two pixels along the direction in which said plurality of first gate signal lines are formed, are adjacent with one of said plurality of power source supply lines therebetween; and

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT of each of said two pixels are connected to said power source supply line.

88 (Withdrawn). A light emitting device according to claim 78,

wherein two pixels along the direction in which said plurality of first gate signal lines are formed, are adjacent with one of said plurality of said second gate lines therebetween;

wherein a gate electrode of said first EL driver TFT and a gate electrode of said second EL driver TFT of each of said two pixels are connected to said plurality of second gate signal lines; and

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT of each of said two pixels are connected to one of said plurality of power source supply lines.

89 (Withdrawn). A light emitting device according to claim 78, wherein said plurality of first gate signal lines and said plurality of second gate signal lines are formed in parallel.

90 (Withdrawn). A light emitting device according to claim 89, wherein said plurality of first gate signal lines and said plurality of second gate signal lines overlap with an insulating film therebetween.



91 (Withdrawn). A light emitting device according to claim 78, wherein said plurality of source signal lines and said plurality of power source supply lines are formed in parallel.

92 (Withdrawn). A light emitting device according to claim 91, wherein said plurality of source signal lines and said plurality of power source supply lines overlap with an insulating film therebetween.

93 (Withdrawn). A light emitting device according to claim 78, wherein said plurality of first gate signal lines and said plurality of power source supply lines are formed in parallel.

94 (Withdrawn). A light emitting device according to claim 93, wherein said plurality of first gate signal lines and said plurality of power source supply lines overlap with an insulating film therebetween.

95 (Withdrawn). A light emitting device according to claim 78, wherein said plurality of second gate signal lines and said plurality of power source lines are formed in parallel.

96 (Withdrawn). A light emitting device according to claim 95, wherein said plurality of second gate signal lines and said plurality of power source supply lines overlap with an insulating film therebetween.

97. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;  
wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element, a switching TFT, an erasure TFT, a first EL driver TFT, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel with said switching TFT;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT; and

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT.

98. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element, a switching TFT, an erasure TFT, a first EL driver TFT, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate

signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT;

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT; and

wherein said gate electrode of said first EL driver TFT and said gate electrode of said second EL driver TFT are electrically connected with each other.

99. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element, a switching TFT, an erasure TFT, a first EL driver TFT, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel with said switching TFT;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT;

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT; and

wherein adjacent pixels have linear symmetry with a power source supply line as a center.

100. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element, a switching TFT, an erasure TFT, a first EL driver TFT, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT;

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT;

wherein said gate electrode of said first EL driver TFT and said gate electrode of said second EL driver TFT are electrically connected with each other; and

wherein adjacent pixels have linear symmetry with a power source supply line as a center.

101. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element, a switching TFT, an erasure TFT, a first EL driver TFT, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel with said switching TFT;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT;

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT; and

wherein said first EL driver TFT and said second EL driver TFT are driven within a linear region in a digital driving method.

102. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element, a switching TFT, an erasure TFT, a

first EL driver TFT, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT;

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT;

wherein said gate electrode of said first EL driver TFT and said gate electrode of said second EL driver TFT are electrically connected with each other; and

wherein said first EL driver TFT and said second EL driver TFT are driven within a linear region in a digital driving method.

103. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element, a switching TFT, an erasure TFT, a first EL driver TFT, and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel with said switching TFT;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT;

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT; and

wherein said first EL driver TFT and said second EL driver TFT are driven within a saturated region in an analog driving method.

104. (New) A light emitting device comprising a source signal line driver circuit, a first gate signal line driver circuit, a second gate signal line driver circuit, and a pixel portion;

wherein said pixel portion comprises a plurality of pixels;

each of said plurality of pixels comprises an EL element; a switching TFT; an erasure TFT; a first EL driver TFT; and a second EL driver TFT;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is electrically connected with a first gate signal line connected with said first gate signal line driver circuit;

wherein a gate electrode of said erasure TFT is electrically connected with a second gate signal line connected with said second gate signal line driver circuit;

wherein a gate electrode of each of said first EL driver TFT and said second EL driver TFT is

electrically connected with one of source and drain regions of said switching TFT and one of source and drain regions of said erasure TFT;

wherein said EL element is controlled by said first EL driver TFT and said second EL driver TFT;

wherein said gate electrode of said first EL driver TFT and said gate electrode of said second EL driver TFT are electrically connected with each other; and

wherein said first EL driver TFT and said second EL driver TFT are driven within a saturation region in an analog driving method.

105. (New) A light emitting device according to claim 97, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

106. (New) A light emitting device according to claim 98, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

107. (New) A light emitting device according to claim 99, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

108. (New) A light emitting device according to claim 100, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

109. (New) A light emitting device according to claim 101, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.



110. (New) A light emitting device according to claim 102, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

111. (New) A light emitting device according to claim 103, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

112. (New) A light emitting device according to claim 104, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

113. (New) A light emitting device according to claim 97, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

114. (New) A light emitting device according to claim 98, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

115. (New) A light emitting device according to claim 99, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

116. (New) A light emitting device according to claim 100, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

117. (New) A light emitting device according to claim 101, wherein said source signal line

driver circuit comprises a shift register, a first latch, and a second latch.

118. (New) A light emitting device according to claim 102, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

119. (New) A light emitting device according to claim 103, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

120. (New) A light emitting device according to claim 104, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

121. (New) A light emitting device according to claim 97, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

122. (New) A light emitting device according to claim 98, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

123. (New) A light emitting device according to claim 99, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

124. (New) A light emitting device according to claim 100, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

125. (New) A light emitting device according to claim 101, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

126. (New) A light emitting device according to claim 102, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

127. (New) A light emitting device according to claim 103, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

128. (New) A light emitting device according to claim 104, wherein said first latch or said second latch comprises two clocked inverters and two inverters.

129. (New) A light emitting device according to claim 97, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

130. (New) A light emitting device according to claim 98, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

131. (New) A light emitting device according to claim 99, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

132. (New) A light emitting device according to claim 100, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

133. (New) A light emitting device according to claim 101, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

134. (New) A light emitting device according to claim 102, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

135. (New) A light emitting device according to claim 103, wherein said light emitting device is one of a computer, a video camera, and a DVD player.

136. (New) A light emitting device according to claim 104, wherein said light emitting device is one of a computer, a video camera, and a DVD player.